DO NOT ENTER: /JP/

02/12/2008

MENTTRANSMITTALLETTER (Large Entity) Docket No. Applicant(s): Joseph G. Walacavage et al. 200-0664 Examiner Customer No. Group Art Unit Confirmation No. Application No. Filing Date

33481

2123

4248

J. Proctor

METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC Invention: CONTROLLER LOGICAL VERIFICATION SYSTEM

COMMISSIONERFOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

September 28, 2001

09/965,905

			CLAI	MSASA	MENDED			
	CLAIMS REI		HIGHE:		NUMBER EXTRA CLAIMS PRESENT		RATE	ADDITIONAL FEE
TOTAL CLAIMS	19	-	21	=	0	x	, \$50.00	\$0.00
INDEP. CLAIMS	. 3	-	3	-	0	x	\$210.00	\$0.00
Multiple Dependent Claims (check if applicable)								\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT								

\boxtimes	No additional	fee is	required to	r amendi	nent
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- A check in the amount of to cover the filing fee is enclosed.
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Daniel H. Bliss (Reg. No. 32,398) [0693.00242] Bliss McGlynn, P.C.

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Record I.D. 81082348

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Dated: January 30, 2008

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

January 30, 2008

Daniel H. Bliss

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